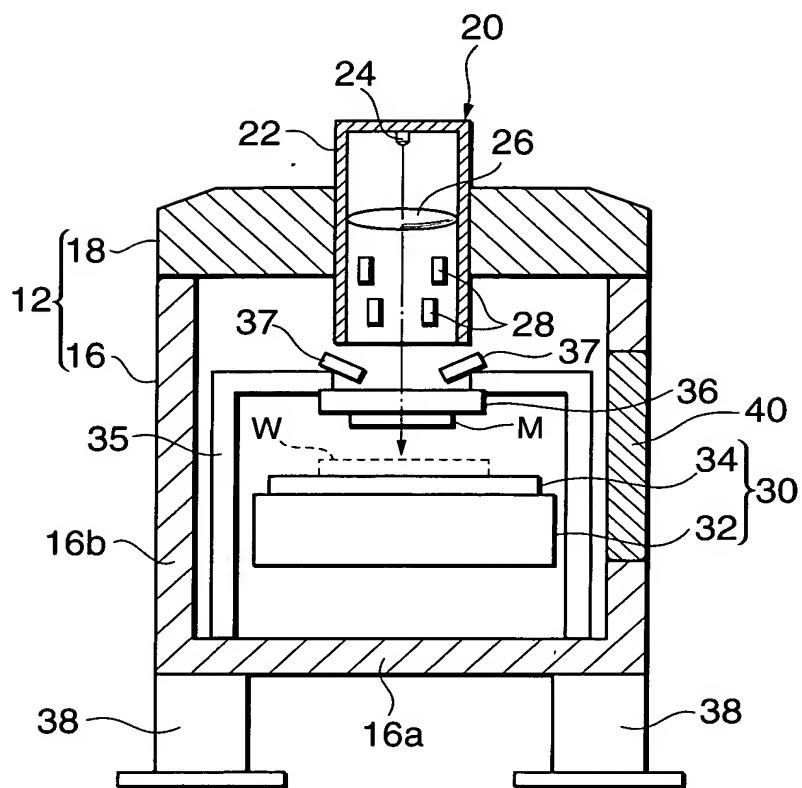
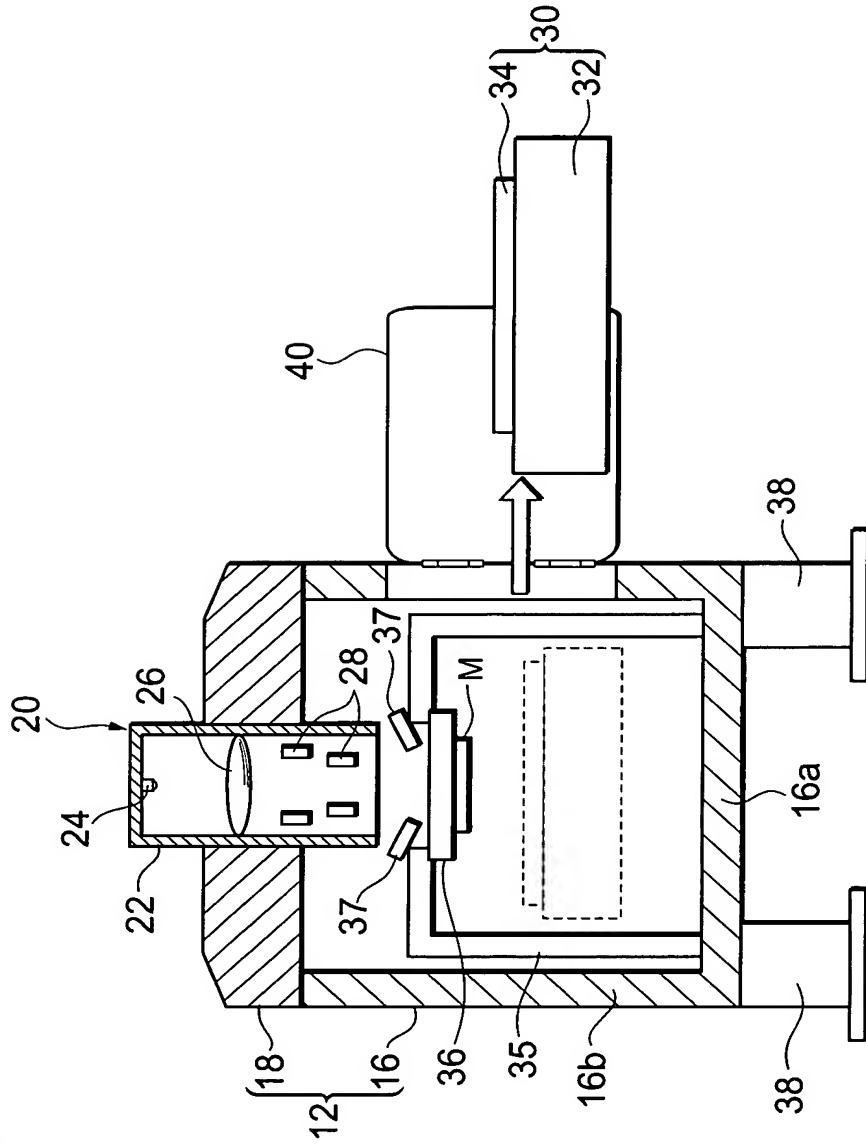
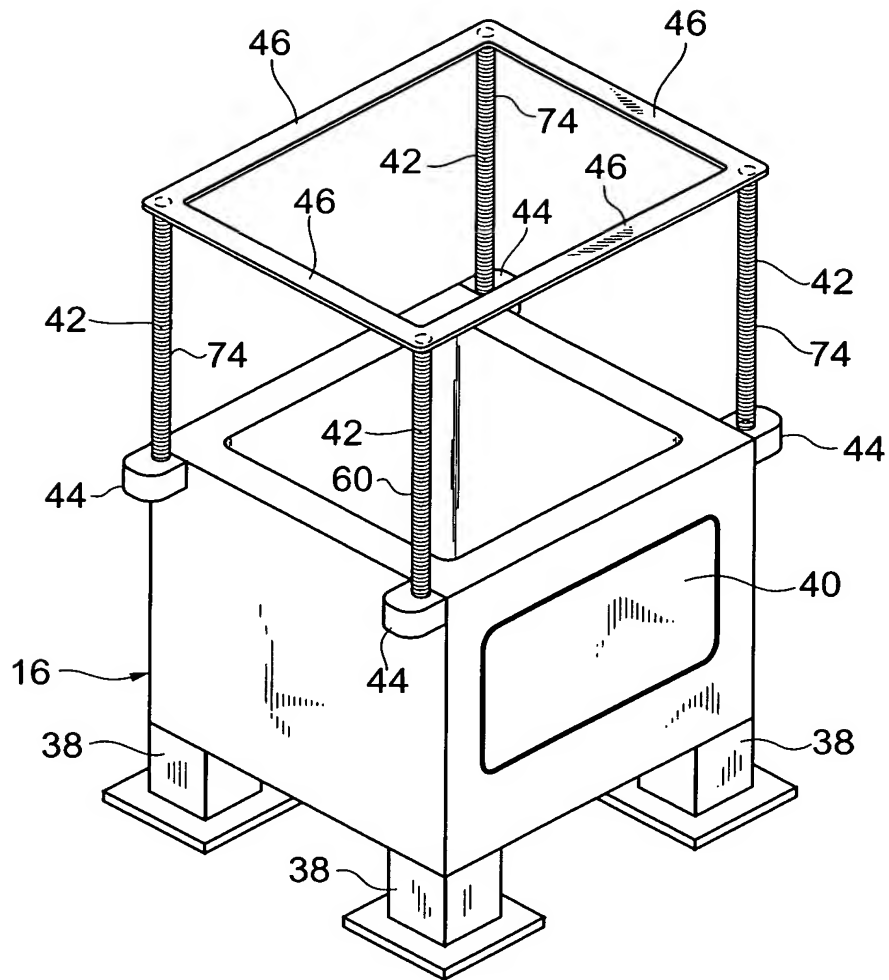


**Fig.2**

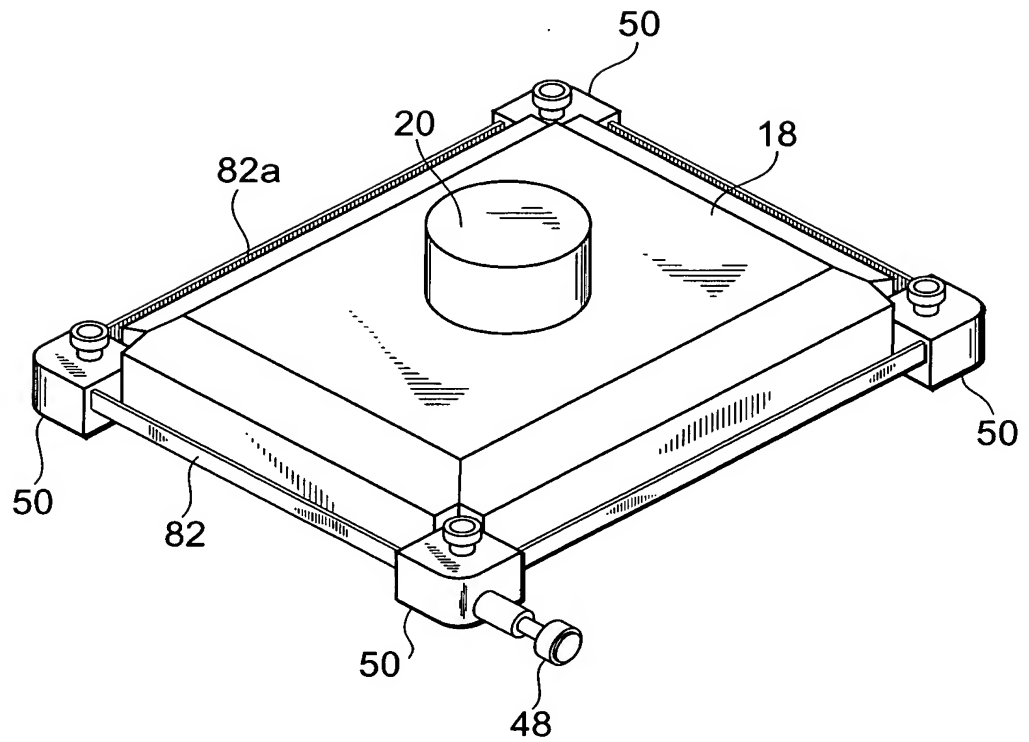




**Fig.4**



**Fig.5**



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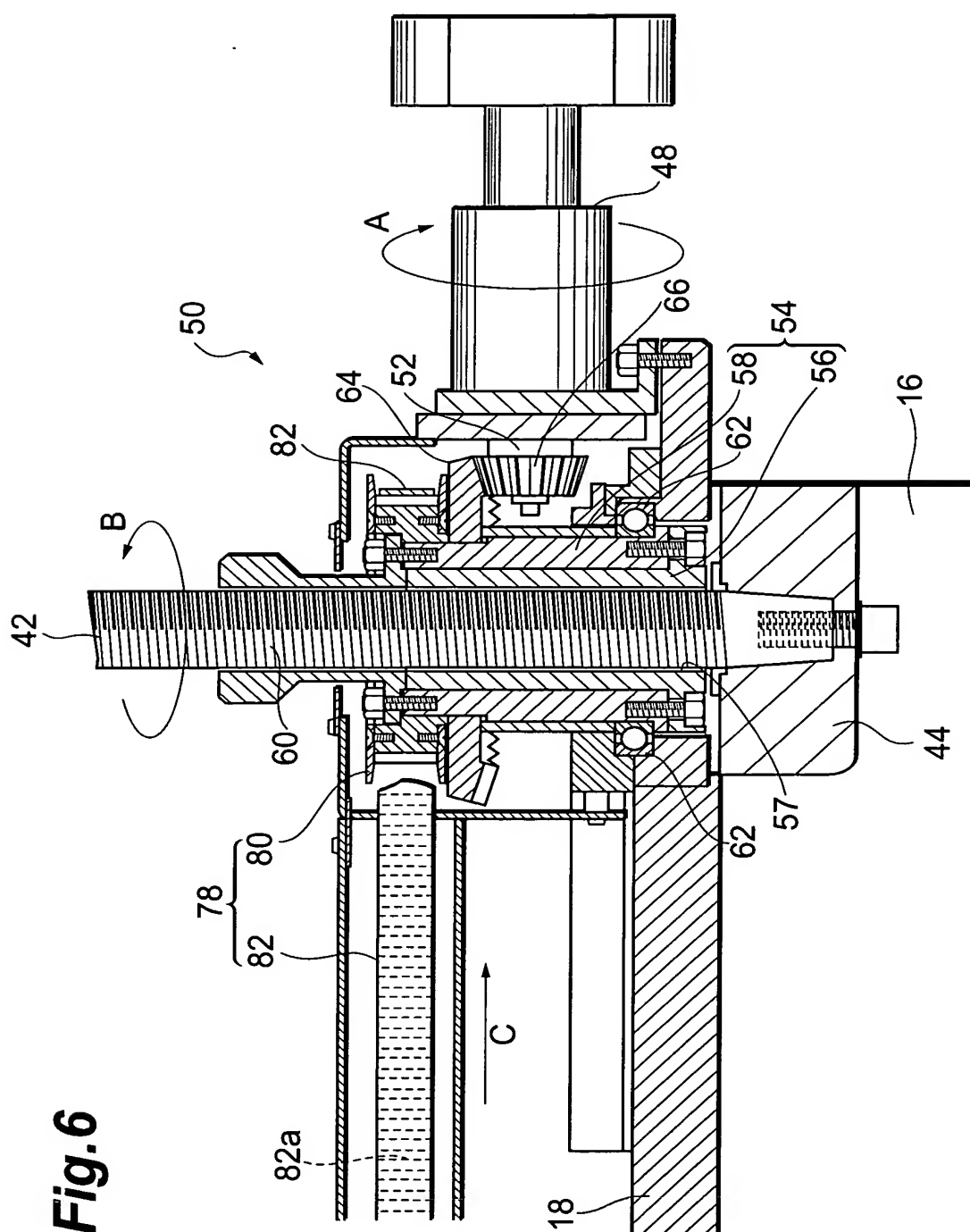
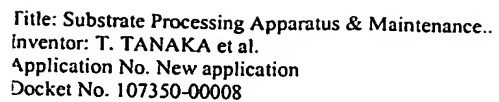
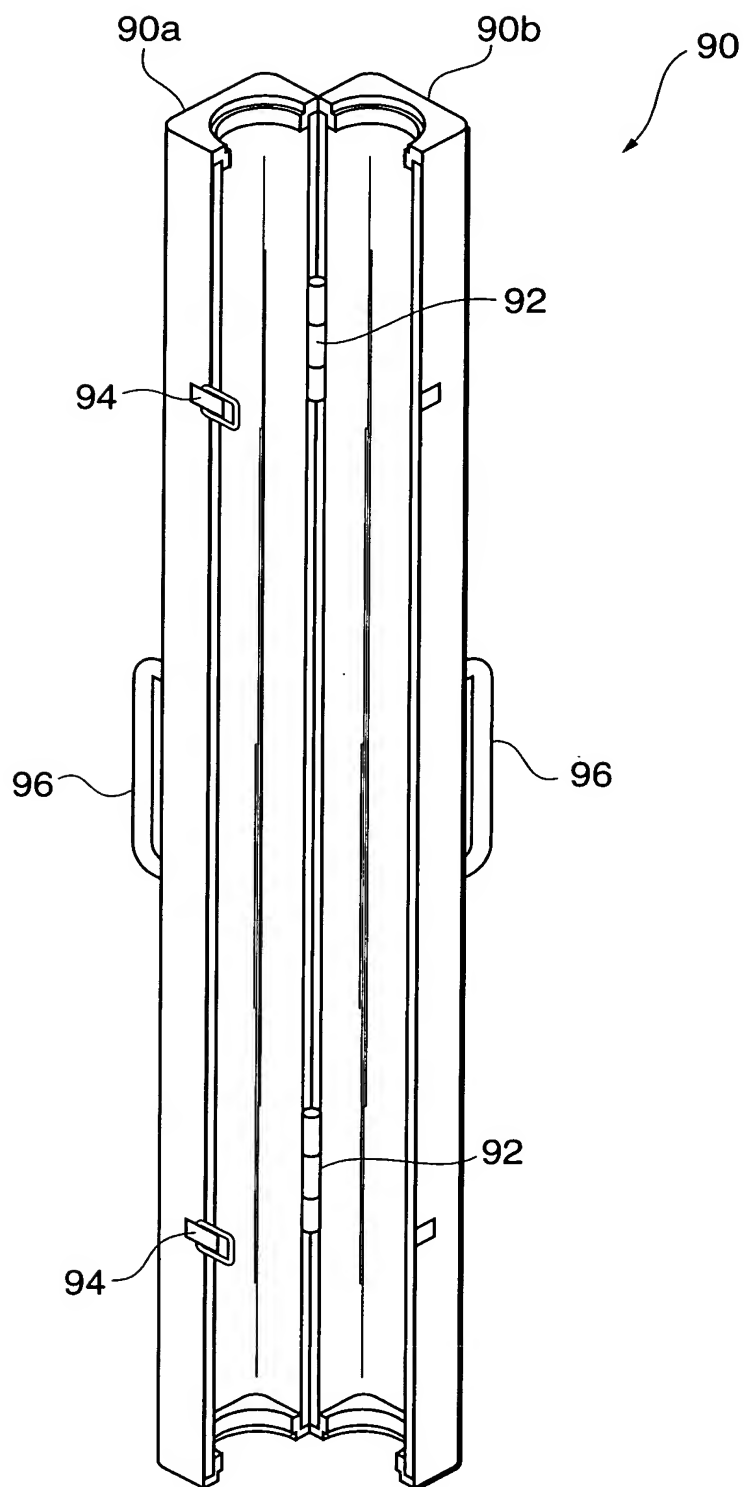


Fig.6



## Fig. 7

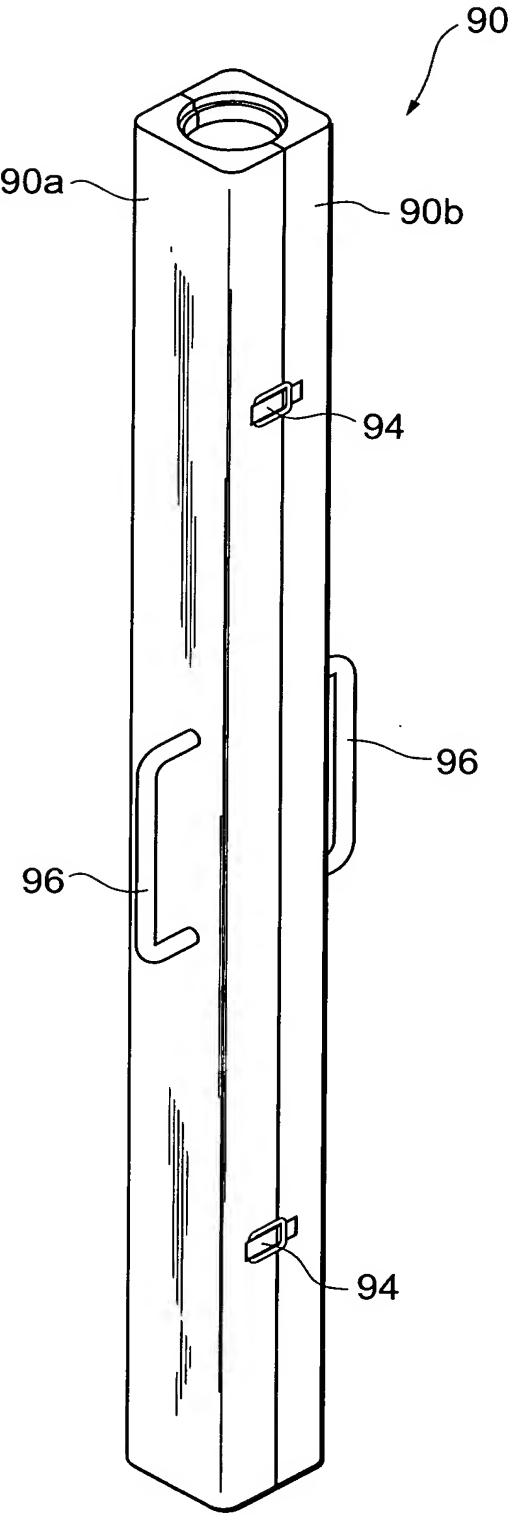
**Fig.8**



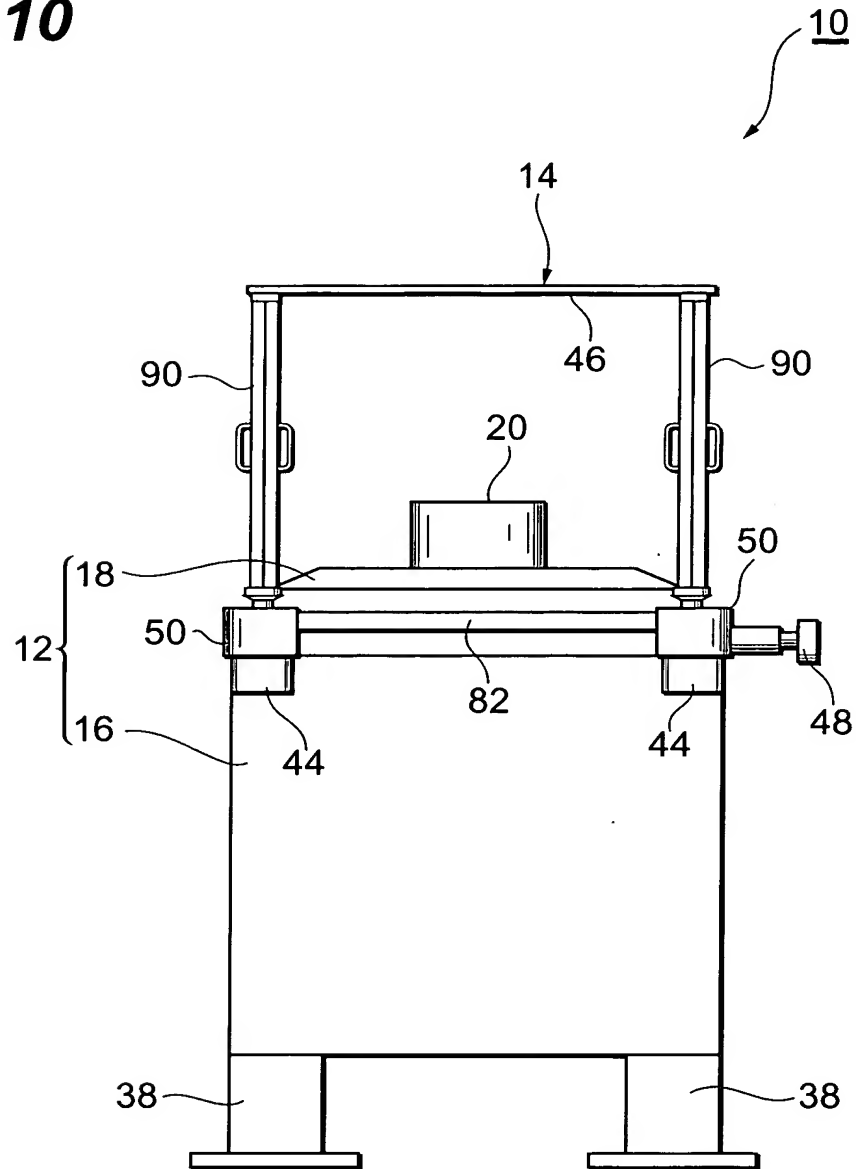


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**Fig.9**



**Fig.10**



Title: Substrate Processing Apparatus & Maintenance..  
Inventor: T. TANAKA et al.  
Application No. New application  
Docket No. 107350-00008

